

FIG . 1

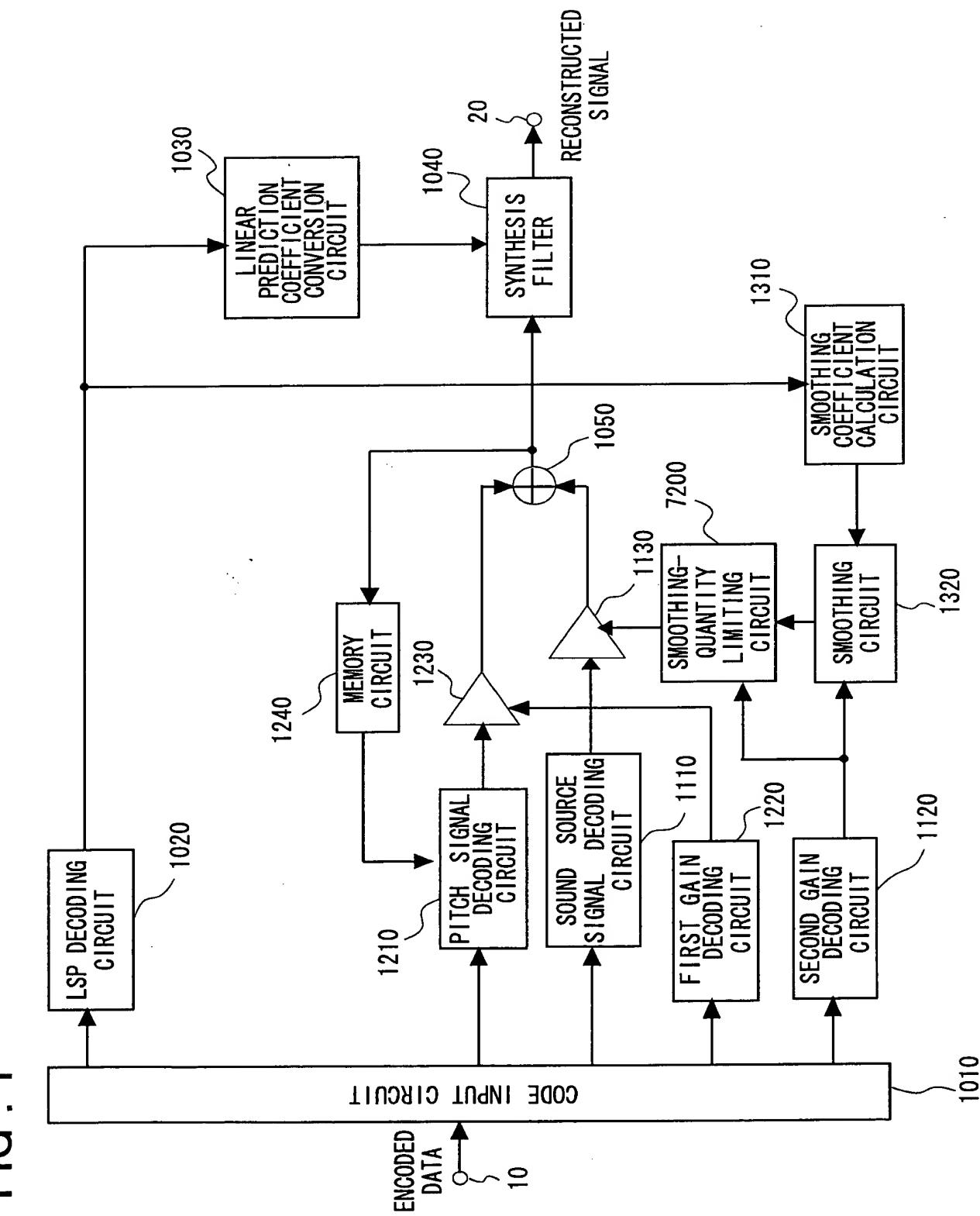


FIG . 2

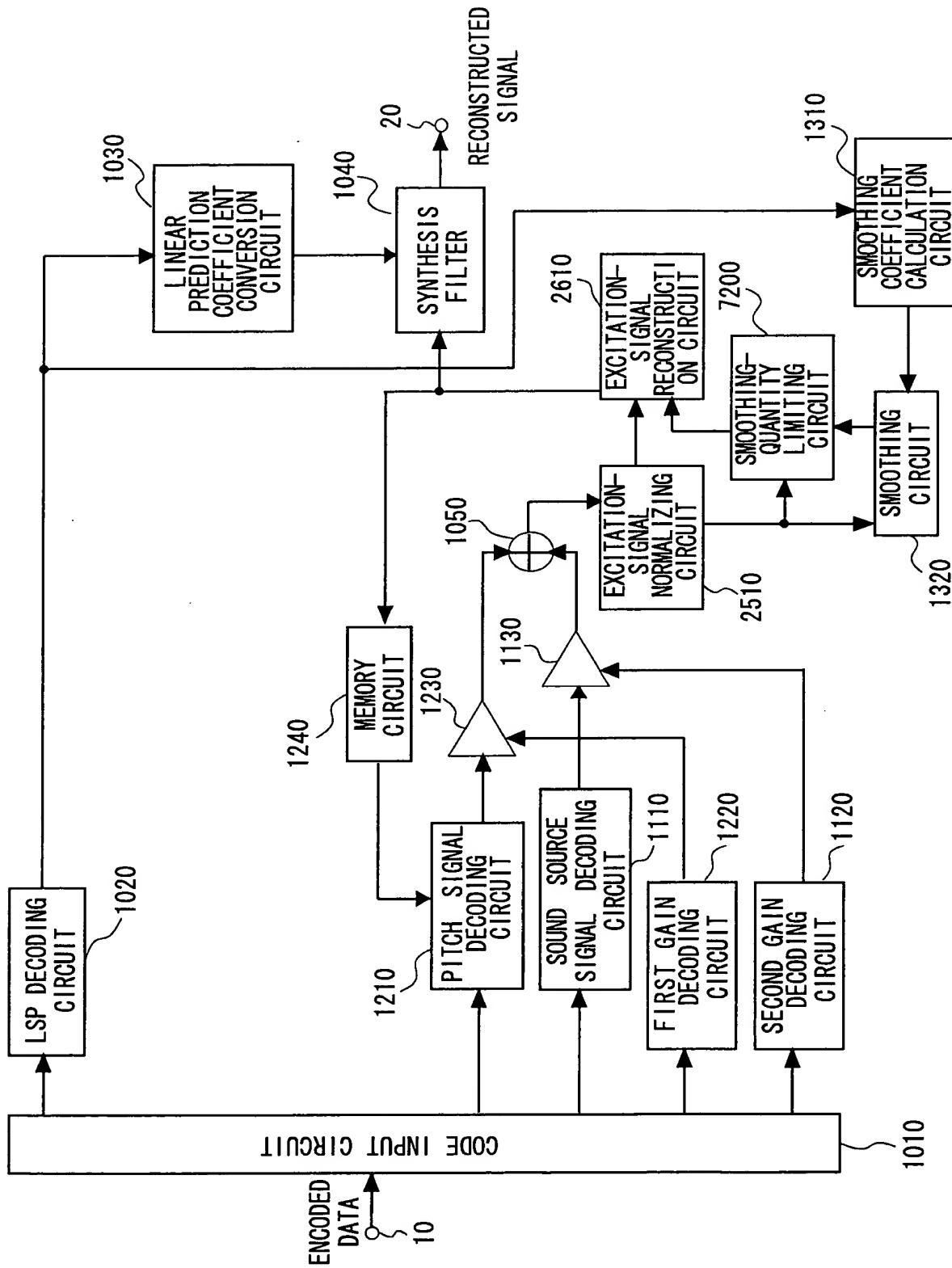
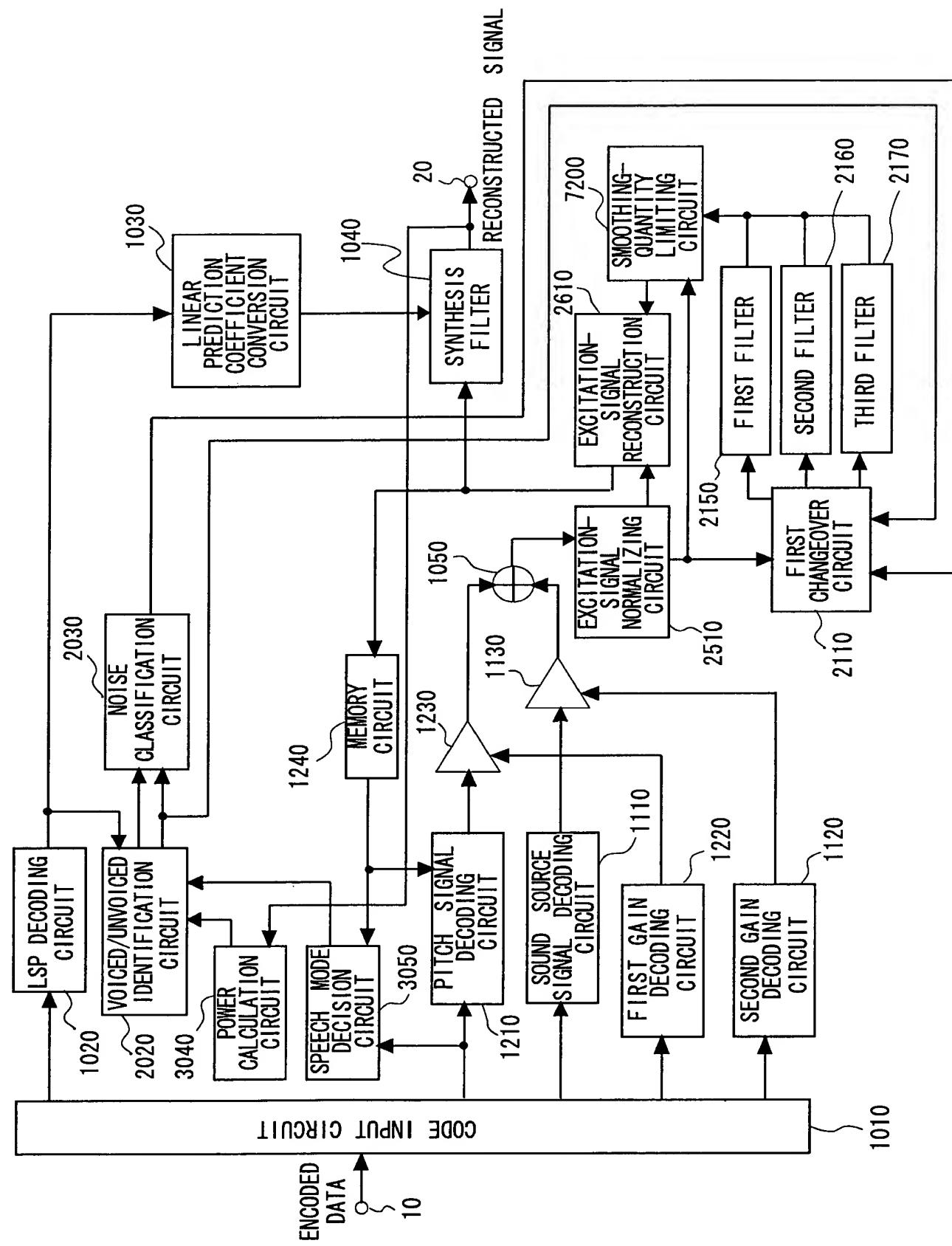


FIG . 3

CODE INPUT CIRCUIT



DRAFT - SEE FIGURE 6

FIG . 4

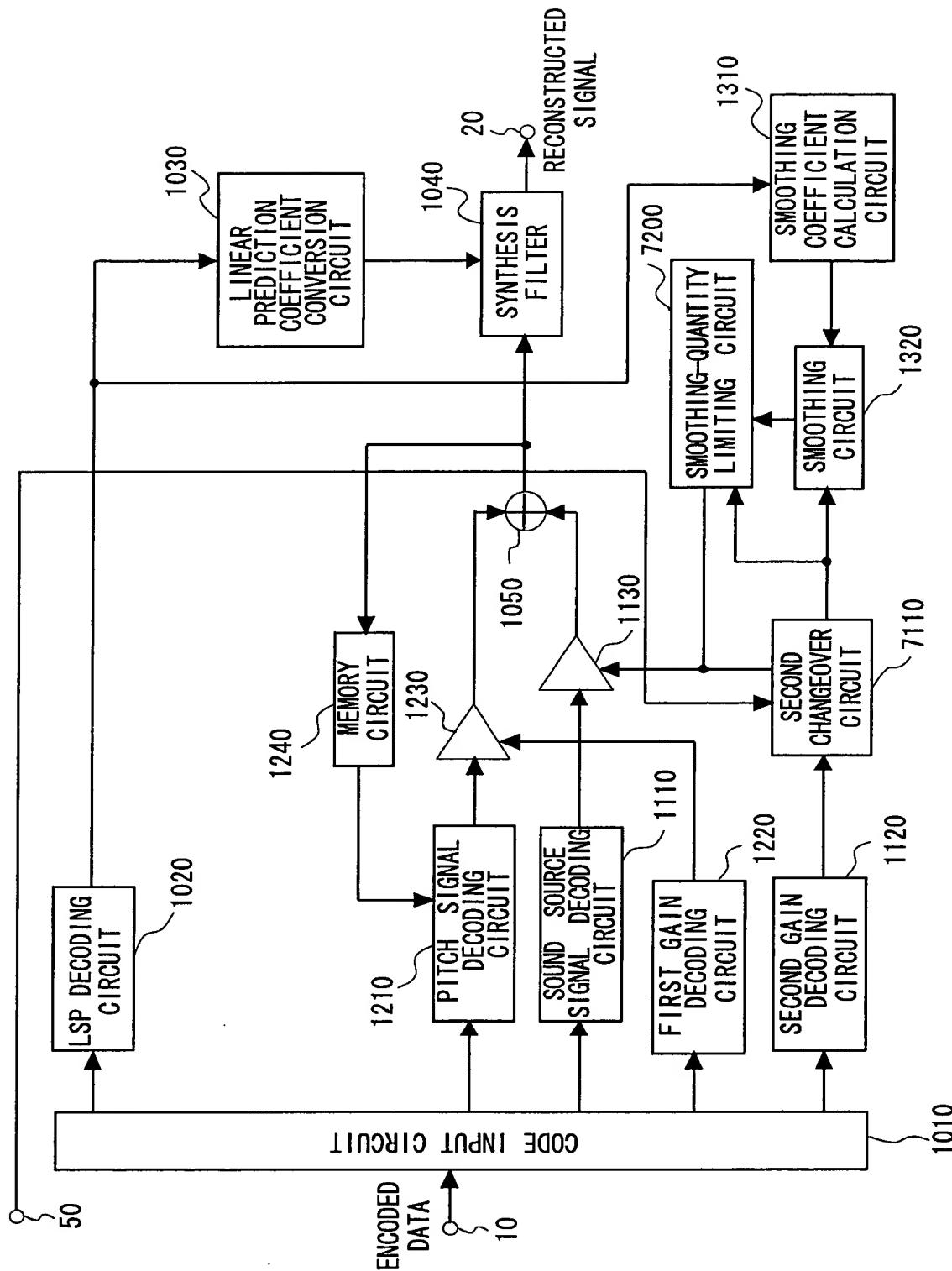


FIG . 5

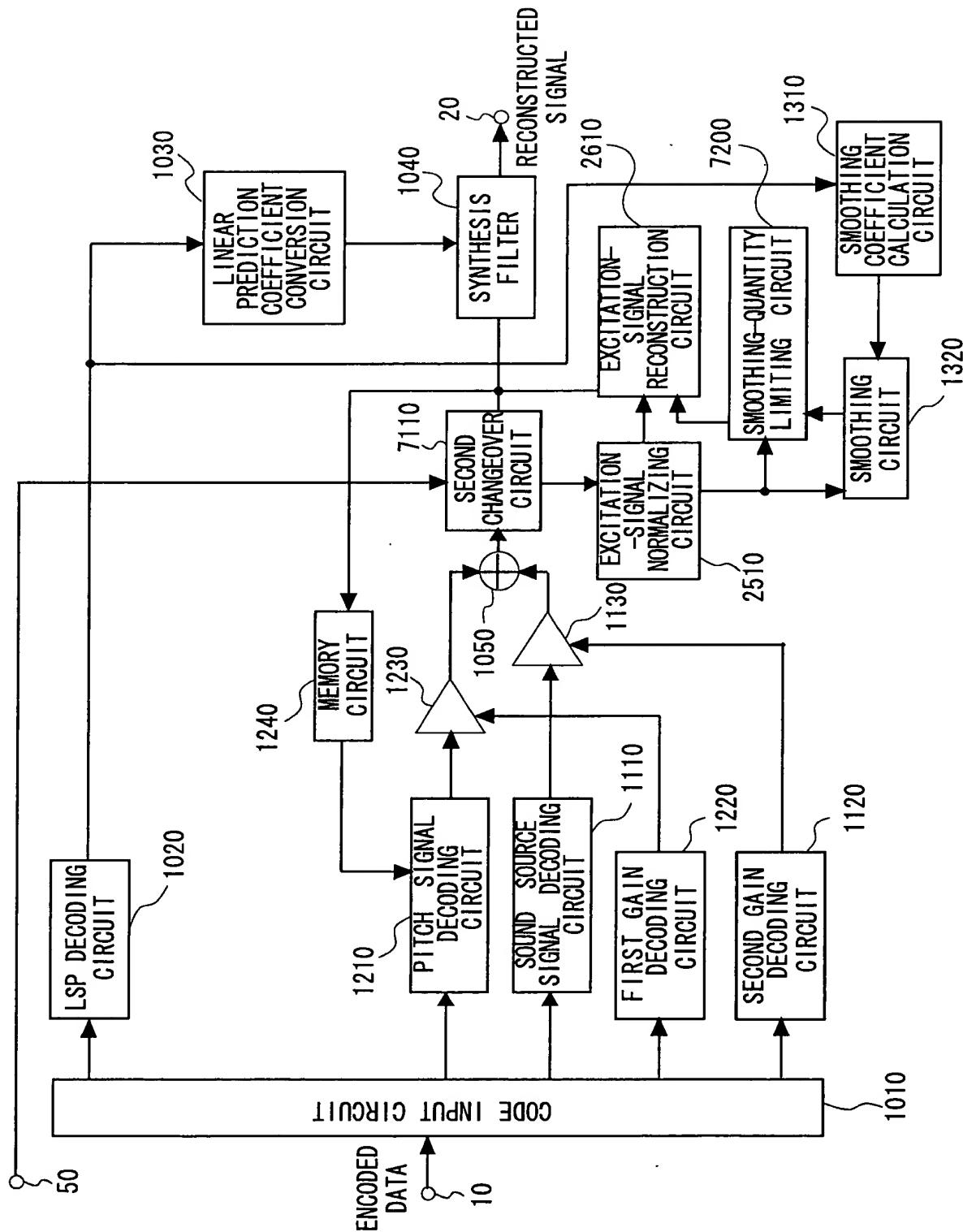


FIG. 6

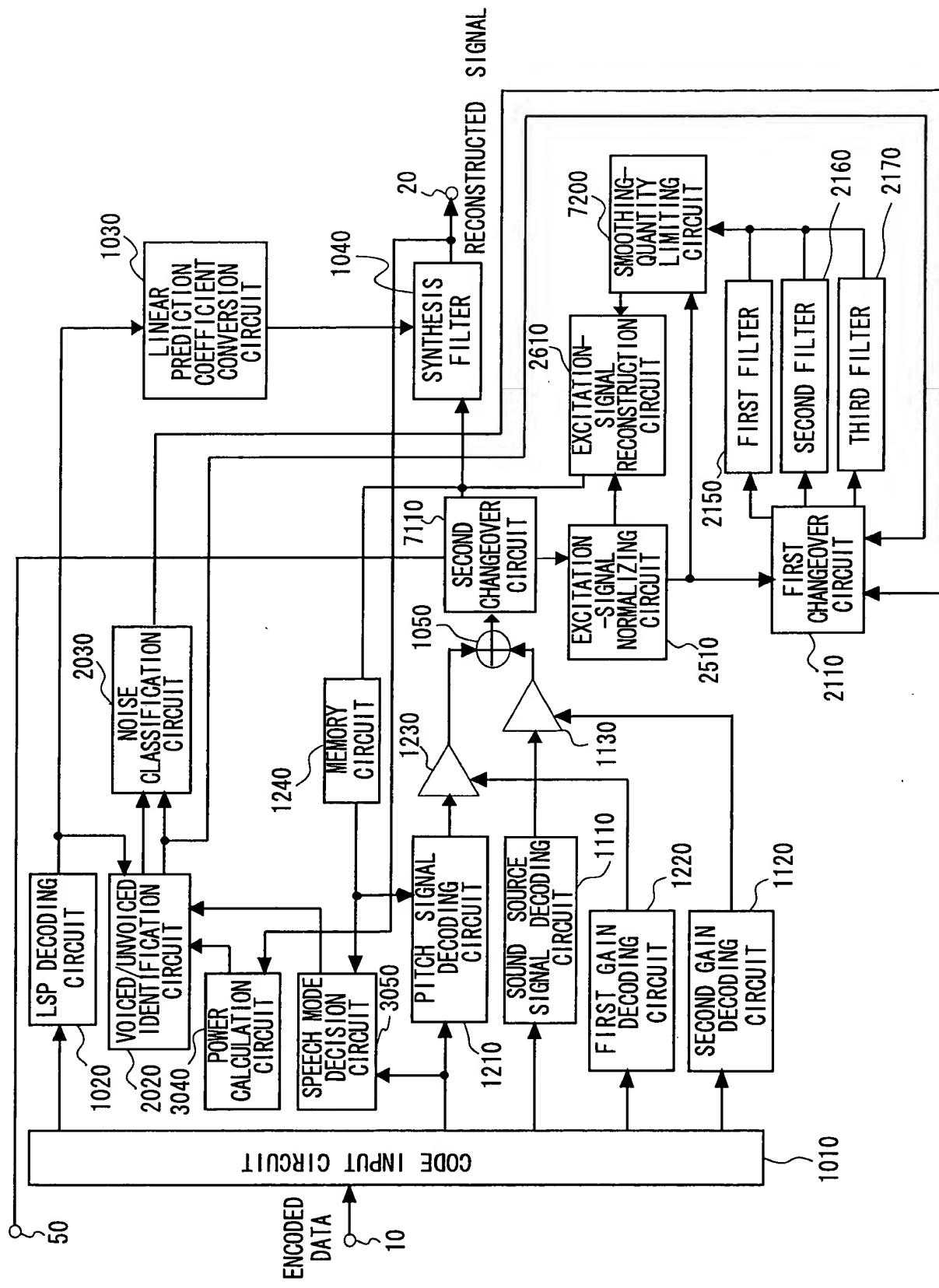


FIG . 7

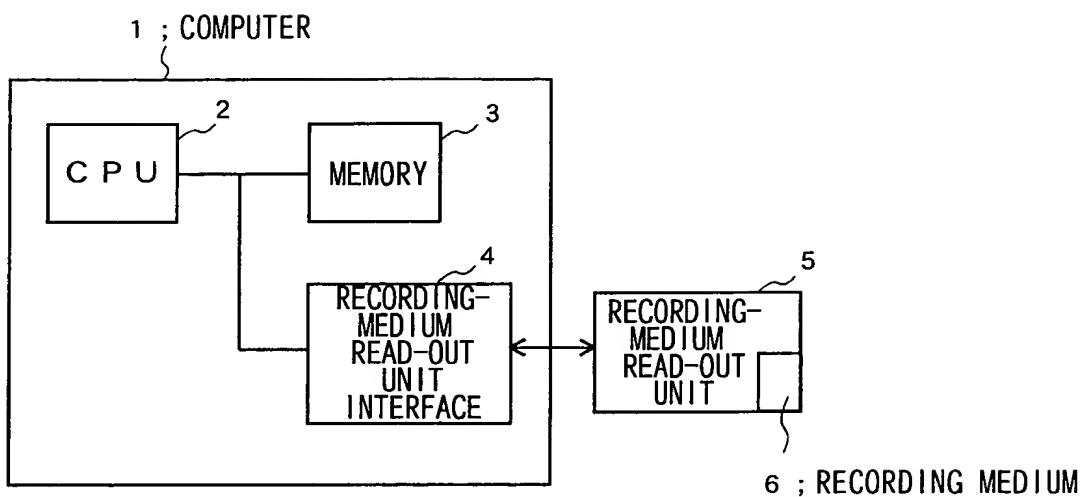


FIG . 8 PRIOR ART

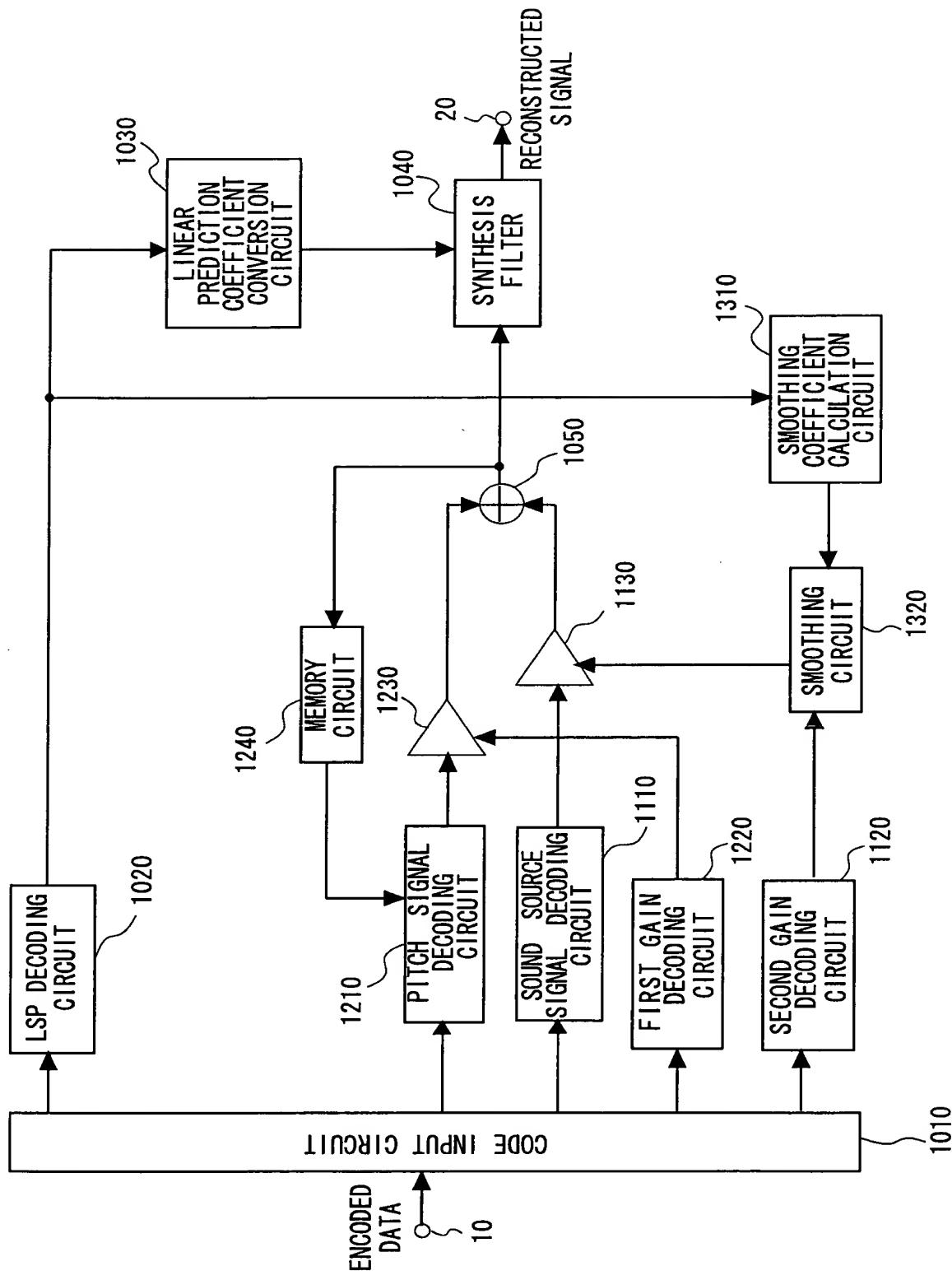


FIG. 9 PRIORITY

